

Radial Leaded PTC

OZRE Series

HF 60 OZRE Series Fuse

RoHS 6 Compliant & Halogen-Free

Application

Line Voltage Power Supply, Transformer and Appliances

Product Features

Continuous Use at voltages up to 240V AC/DC, Low Hold Current, 265V AC/DC Max Interrupt voltage

Operating (Hold Current) Range

50mA - 550mA

Temperature Range

-40°C to 85°C

Maximum Operating Voltage

240V AC/DC

Maximum Interrupt Voltage

265V AC/DC

Agency Approval

TUV (Std. EN60738-1-1, Cert. R50102173)

UL Component (Std. UL1434, File E305051)

UL Conditions of Acceptability:

- 1. These devices have been investigated for use in safety circuits and are suitable as a limiting device.
- 2. These devices have been calibrated to limit the current to 8 amps within 5 seconds, per ANSI/NFPA 70, "National Electrical Code".

LEAD FREE = (Pb)

HALOGEN FREE = **HF**



	Part Number (Bulk)		Trip		Max Current	Rated Voltage	Typical Power	Resistance Tolerance			Agency Approvals	
			Current					Rmin	Rmax	R1max	c FL °us	\triangle
		IH, A	IT, A	Seconds	Imax, A	Vmax, Vdc	Pd, W	Ohms	Ohms	Ohms	C TABUS	TÜV
Α	0ZRE0005FF1C	0.05	0.12	15.0	1.5	240	0.7	18.50	31.00	65.00	Υ	Υ
В	0ZRE0008FF1C	0.08	0.19	15.0	1.5	240	0.8	7.40	12.00	26.00	Y	Υ
С	0ZRE0012FF1C	0.12	0.30	15.0	2.0	240	1.0	3.00	6.50	12.00	Υ	Υ
D	0ZRE0016FF1C	0.16	0.37	15.0	2.0	240	1.4	2.50	4.10	7.80	Υ	Υ
Е	0ZRE0025FF1E	0.25	0.56	18.5	6.0	240	1.5	1.30	2.10	3.80	Υ	Υ
F	0ZRE0033FF1A	0.33	0.74	21.0	6.0	240	1.7	0.83	1.24	2.60	Υ	Υ
G	0ZRE0040FF1A	0.40	0.90	24.0	7.0	240	2.0	0.60	0.97	1.90	Υ	Υ
Н	0ZRE0055FF1A	0.55	1.25	26.0	7.0	240	3.4	0.45	0.73	1.45	Υ	Υ

ΙH Hold Current-maximum current at which the device will not trip in still air at 23°C.

Trip current-minimum current at which the device will always trip in still air at 23°C. Maximum fault current device can withstand without damage at rated voltage (Vmax). lmax

Maximum voltage device can withstand without damage at its rated current. Vmax Typical power dissipated by device when in tripped state in 23°C still air environment. Pd

Minimum device resistance at 23°C. Maximum device resistance at 23°C. Rmax

R1max Maximum device resistance at 23°C, 1 hour after initial device trip, or after being soldered to PCB in end application.

Specifications subject to change without notice



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PTC's - Basic Theory of Operation / "Tripped" Resistance Explanation

Fundamentally, a Bel PTC consists of a block of polymeric material containing conductive filler and bonded between two conductive, planar terminations.

At currents below the device IHOLD rating, AND at temperatures below 100C, the PTC maintains a resistance value below its R1 MAX rating.

As the device's temperature approaches 130C, either due to an increase in ambient temperature or a current exceeding its I TRIP rating, volumetric expansion of the filled polymer breaks apart the majority of conductive pathways across the terminals created by chain contact of adjacent filler particles or device resistance increases sharply by several orders of magnitude.

At the much higher "Tripped" resistance, there is just enough leakage current to allow internal heating to "hold" the device in its tripped state (around 125C) until power is interrupted. Once power is removed, the PTC's core cools and contracts allowing conductive chains to reform and return the device to its low resistance state.

The catalog data for each device specifies a "Typical Power" value. This is the power required to exactly match the heat lost by the tripped device to its ambient surroundings at 23C. By Ohm's Law, power can be stated as: W = E^2/R. Thus the approximate resistance of a "Tripped" PTC can be determined by: R = E^2/W, where "E" is the voltage appearing across the PTC (usually the supply's open circuit voltage), and "W" is the Typical Power value for the particular PTC. Since the PPTC acts to maintain a constant internal temperature, its apparent resistance will change based upon applied voltage and, to a lesser degree, ambient conditions. Consider the following example....

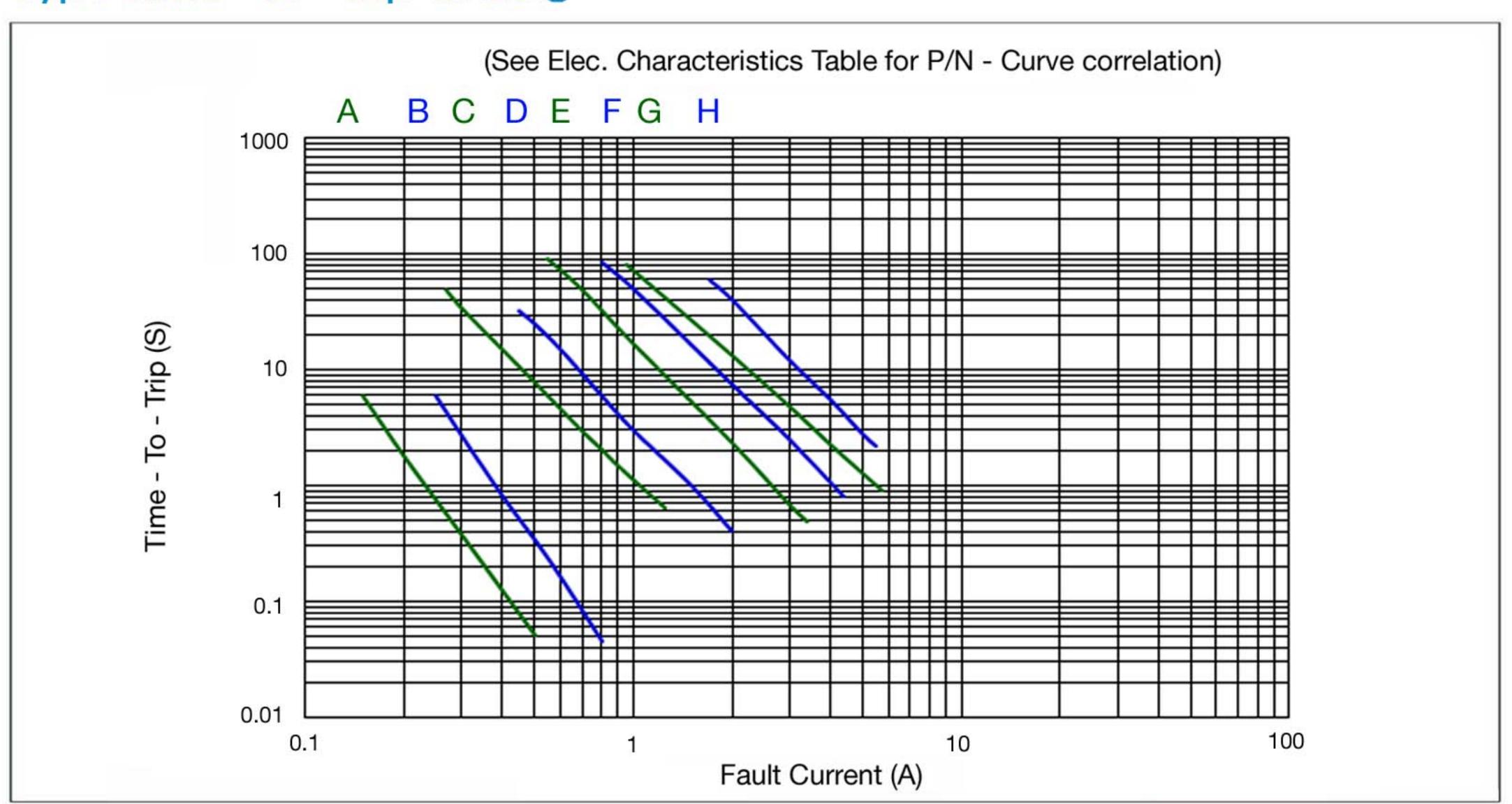
A PTC with a Typical Power of 1 watt protecting a circuit using a 60V supply will demonstrate an apparent, tripped resistance "R" of:

 $R = 60^2/1 = 3,600 \text{ ohms}$

This same tripped device when used to protect a 12V circuit would now present an apparent resistance of: $R = 12^2/1 = 144$ ohms

The value for Typical Power is "typical" because any physical factors that affect heat loss (such as ambient temperature or air convection) will somewhat alter the level of power that the PTC needs to maintain its internal temperature. In short, PTCs do not exhibit a constant, quantifiable tripped resistance value.

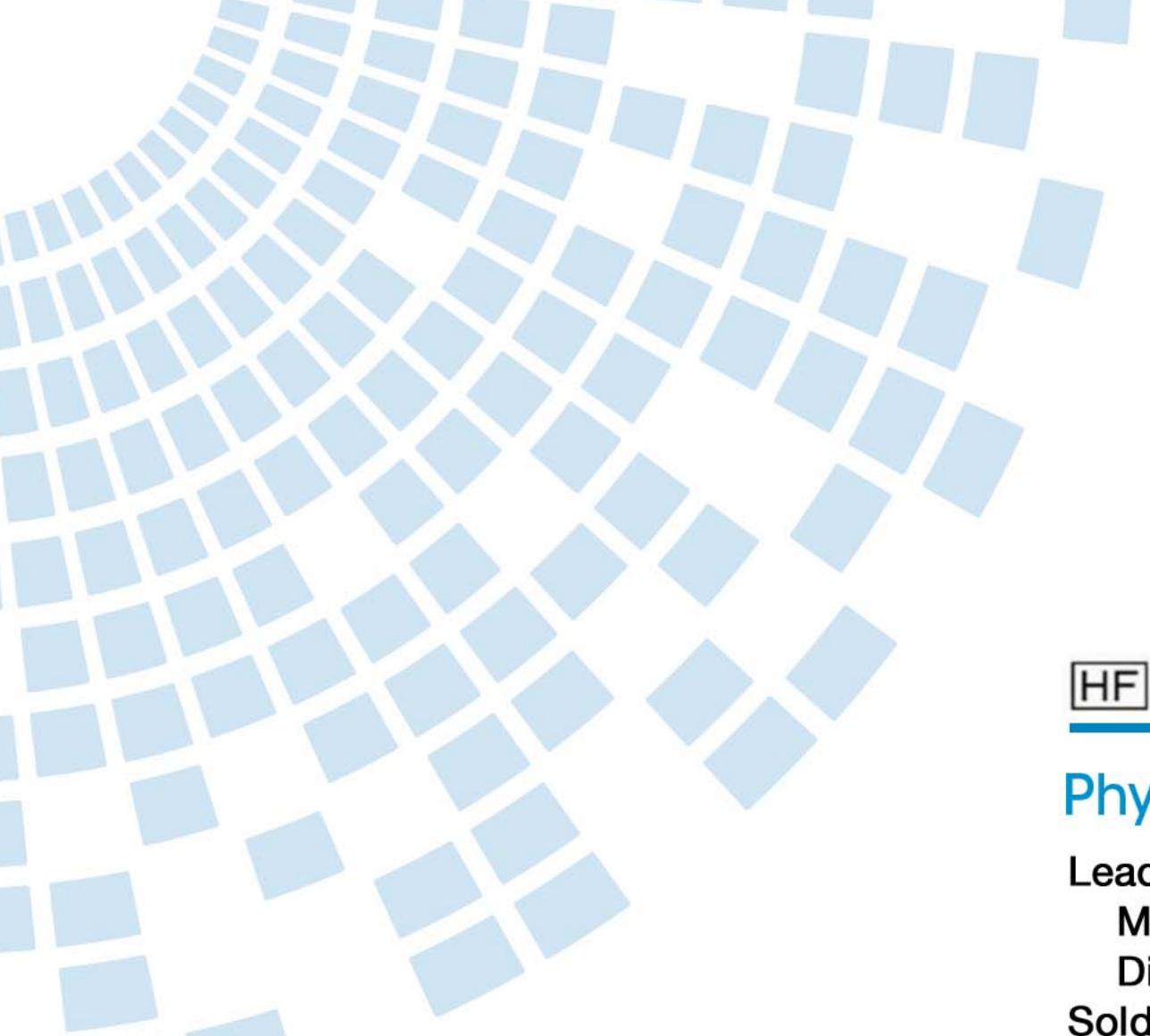
Type Time - To - Trip at 23°C



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All dimensions in mm.

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Physical Specifications

Lead material:

Matte tin plated copper, size / diameter as shown in Drawings and Table under Product Dimensions.

Soldering characteristics

MIL-STD-202, Method 208H.

Insulating coating

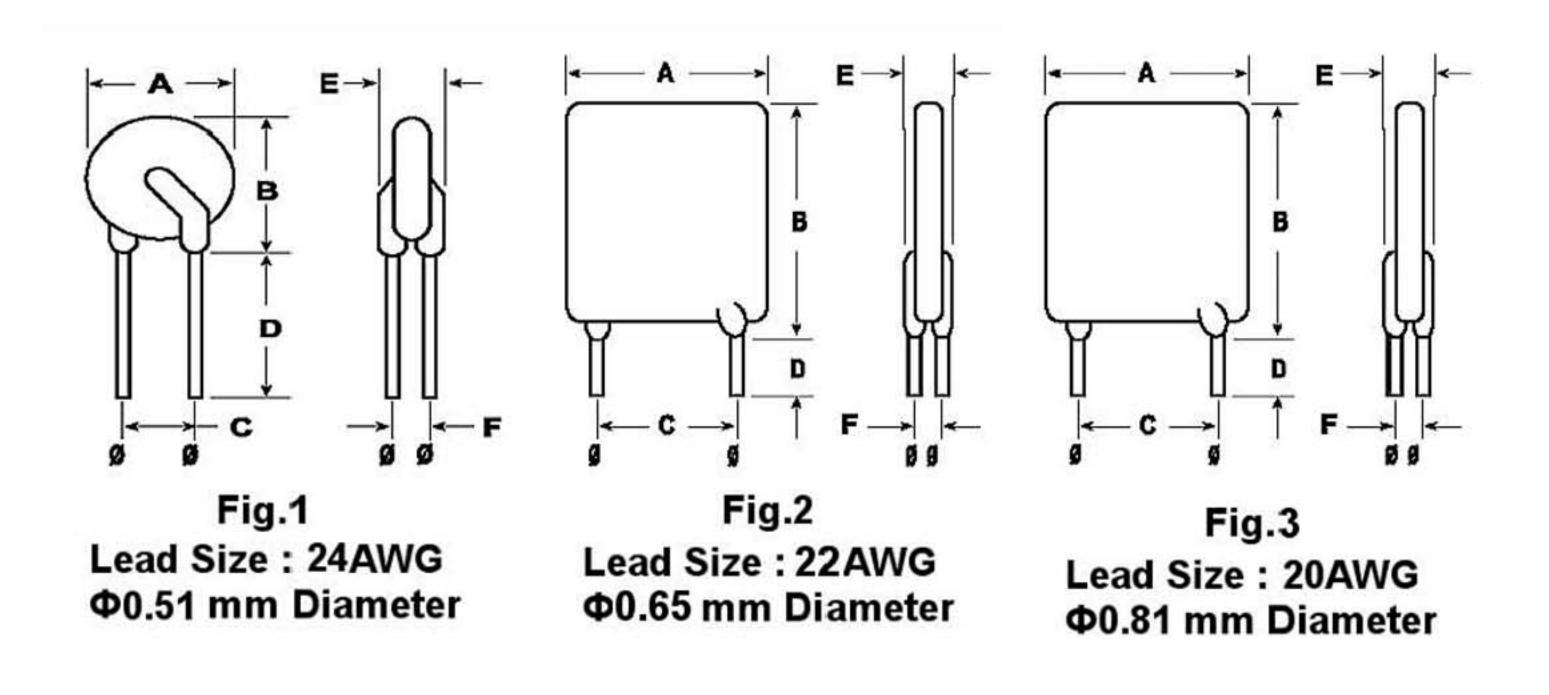
Flame retardant epoxy, meets UL-94-V-0 requirements.

0ZRE0055FF

PTC Marking

"bel" or "b", , IH code and "RE".

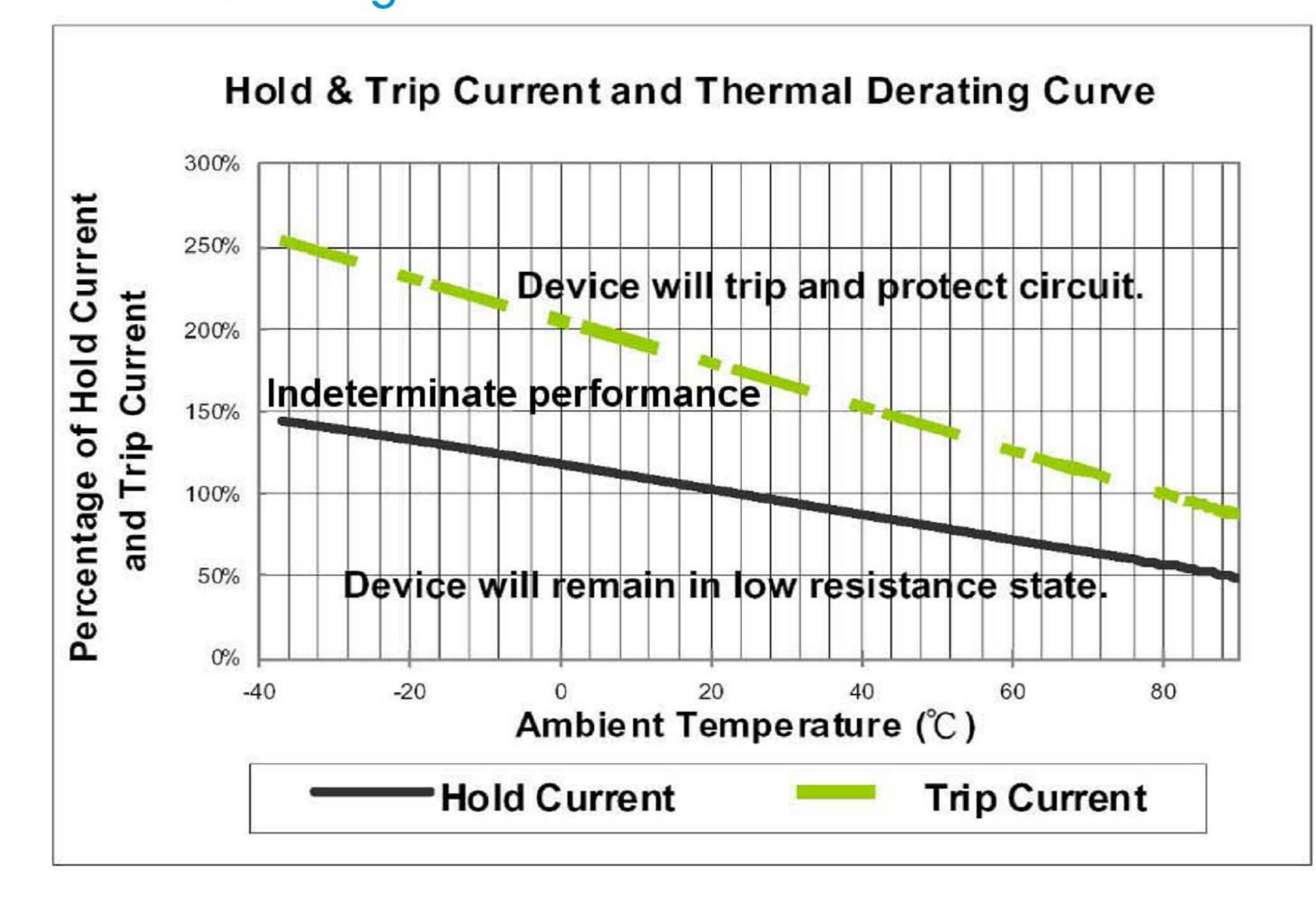
Product Dimensions



В Fig. Part Number Max Typical Typical Max Min Max 0ZRE0005FF 8.3 10.7 5.1 7.6 3.8 1.6 to 0ZRE0012FF 3.8 9.9 12.5 5.1 7.6 1.6 0ZRE0016FF 0ZRE0025FF 9.6 17.4 5.1 7.6 3.8 1.8 0ZRE0033FF 11.4 16.5 5.1 7.6 3.8 1.8 0ZRE0040FF 11.5 19.5 5.1 7.6 3.8 1.8

21.7

Thermal Derating Curve



Cautionary Notes

14.0

 Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.

5.1

7.6

4.1

1.9

- These Polymer PTC (PPTC) devices are intended for protection against occasional overcurrent/ overtemperature fault conditions and may not be suitable for use in applications where repeated and/or prolonged fault conditions are anticipated.
- Avoid contact of PTC device with chemical solvent.
 Prolonged contact may adversely impact the PTC performance.
- 4. These PTC devices may not be suitable for use in circuits with a large inductance, as the PTC trip can generate circuit voltage spikes above the PTC rated voltage.
- These devices are intended for use in DC voltage applications only. Use in AC voltage applications should be first discussed with Bel Fuse engineering.
- 6. Not recommended for use on potted or conformal coated PCB's. Restriction of free air flow could affect electrical performance and/or result in device failure. Consult Bel Fuse engineering.
- 7. In the "Indeterminate Performance / grey zone", tripping may occur but cannot be relied upon. For special circumstances considering use within this region, consult Bel Fuse Engineering.

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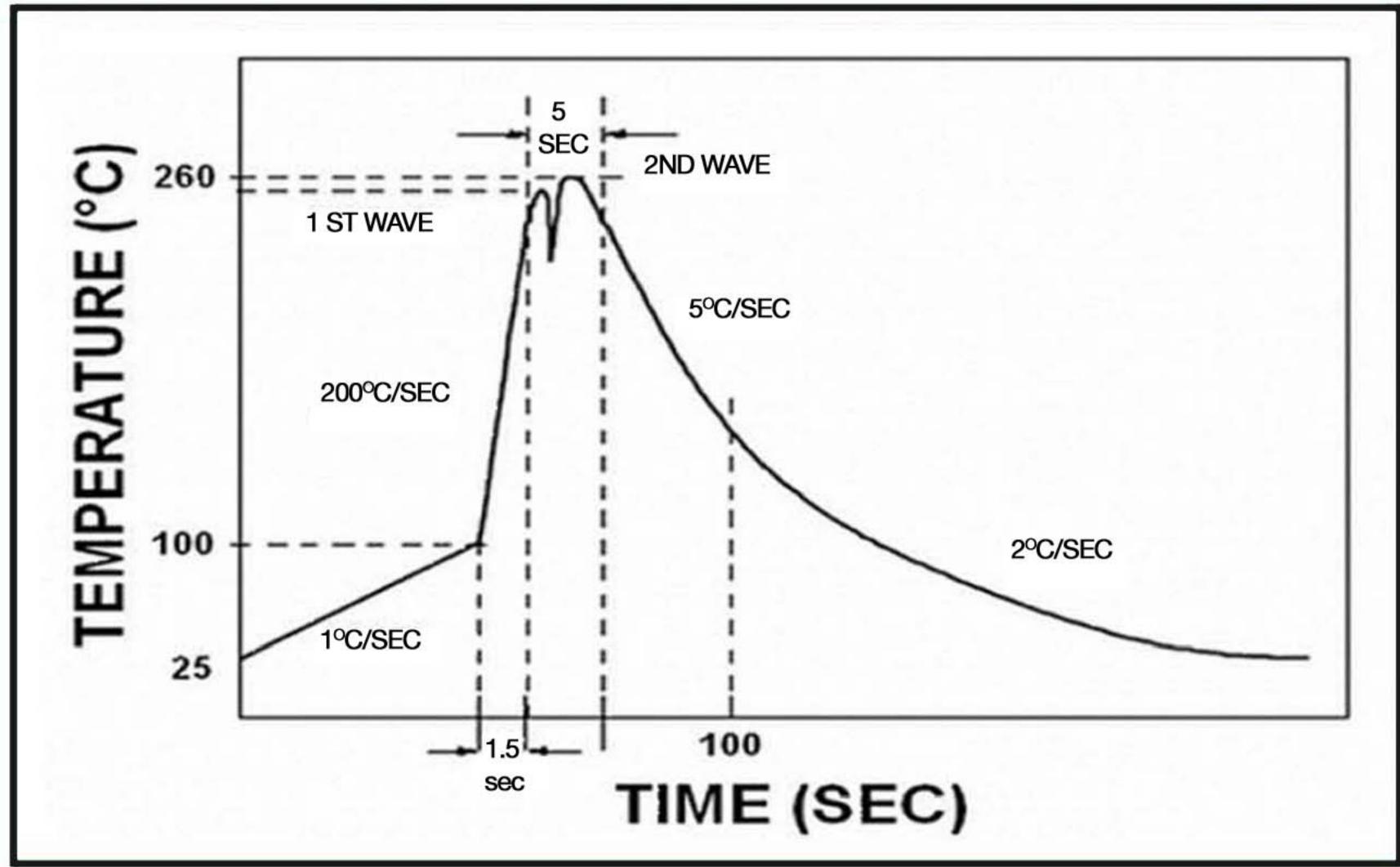
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Soldering Parameters

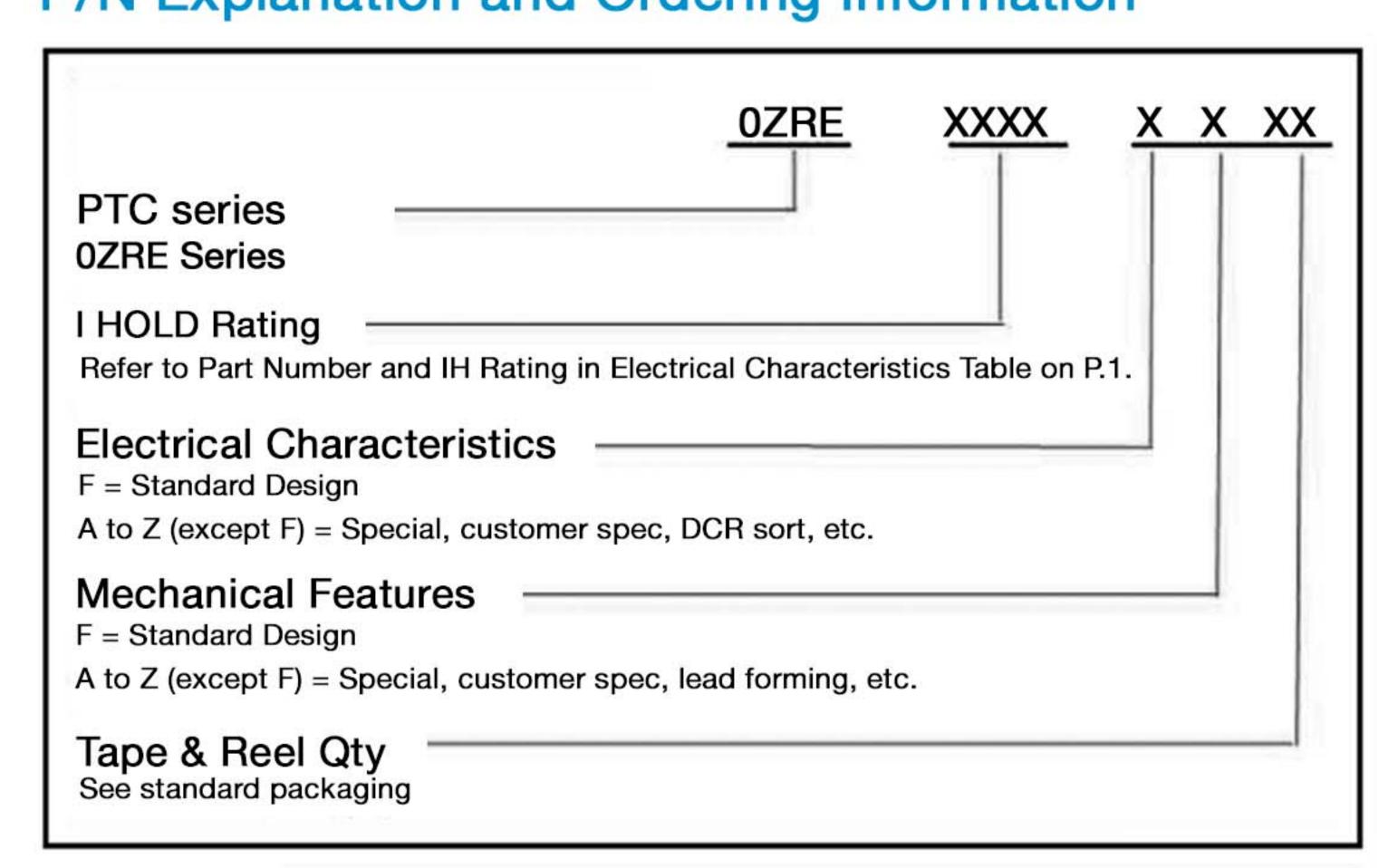
Lead-free Wave Soldering Profile					
Wave Solder Parameter					
Average ramp-up rate	200°C / second				
Heating rate during preheat	typical 1 - 2°C / second Max 4°C / second				
Final preheat temperature	within 125°C of soldering temperature				
Peak temperature Tp	260°C				
Time within +0°C / -5°C of actual peak temperature	5 seconds				
Ramp-down rate	5°C / second max.				



Standard Packaging

D/NI	Вι	ılk	Reel/Tape		
P/N	Pcs/Box	P/N Code	Pcs/Reel	P/N Code	
0ZRE0005FF					
U C	2000	1C	2000	2C	
0ZRE0016FF					
0ZRE0025FF	3000	1E	1500	2B	
0ZRE0033FF					
3 	1000	1A	1500	2B	
0ZRE0040FF	i				
0ZRE0055FF	1000	1A	1000	2A	

P/N Explanation and Ordering Information



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POWER SOLUTIONS & SOLUTIONS & PROTECTION

a bel group

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